

Figure 1. Visitors organized by CPCA.



Figure 2. HKPCA visitors.



Figure 3. The JPCA reception.



Figure 4. Mr. Kumar (CPCA), Mr. Weinhold and Mr. Rosario (EIPC). (from left to right)



Figure 5. Dr. Nakahara, Mr. Vousten and Mr. Berkel of Ruwel AG. (from left to right)



Figure 6. Richard McCann (MacDermid), Michael Moisan (TTM), Dr. Nakahara and Hardeep Herr (ECT). (from left to right)



Figure 7. CMK showed an example of wafer level package integration for watch module.



Figure 8. One segment tuner modules for cell phones (Toppan-NEC).



Figure 9. Paragon 8800 with automated loader.



Figure 10. DIIMPACT DXP-3502 for PSR by ORC.



Figure 11. IMPREX by Fuji Film.



Figure 12. Mercurex by DNS.



Figure 13. HVM's "Digital Exposure" machine.



Figure 14. HVM 4-Table CO₂ drilling machine.



Figure 15. 190% efficiency with 50% more space.



Figure 16. HVM advertising UV/YAG laser.



Figure 17. Mitsubishi Electric 4-Beam CO₂ laser.



Figure 18. Mitsubishi Model ML605GTWII-5150U for CO₂ Copper Direct.



Figure 19. The Schmol version (router) of the independent axis drive machine.



Figure 20. Six-spindle HVM version of the independent axis drive machine.



Figure 21. Micro Craft model MJ6151D1.



Figure 22. Panel flattening scheme.



Figure 23. Cross-section of 8L FVSS boards with L/S=50/50µm (Ibiden).



Figure 24. 3+4+3 Cell Phone board with 50/50µm (Toppan-NEC).



Figure 25. 1+8+1 for Panasonic NB "Lets Note" (Toppan-NEC).



Figure 26. One-Segment TV Tuner (2+2+2) attached to motherboard for cell phone.



Figure 27. 37" PDP TV (Panasonic) Total PCB: $1.2m^2$, estimated worth \$40/set (DS FR-4. Tuner board, SS FR-1 Power Supply, 6L FR-4 Main board, 2L Fr-4 LCD display etc).





Irem	Class	2006	2008	2010	2012	2014	201
Min. Line Width (μm)	A	30	30	20	20	15	15
	В	20	15	15	7	7	7
	С	15	7	7	5	5	5
Min. Space Width (µm)	A	30	30	20	20	15	15
	В	20	15	15	7	7	7
	С	15	7	7	5	5	5
Min. Land Diameter for Laser Micro Via (μm)	A	110	90	90	80	80	70
	В	90	70	60	60	50	5
	С	70	60	50	40	40	3

Figure 29. Technology Trend of Build-Up IC Substrate.